

FIG. 1 (PRIOR ART)

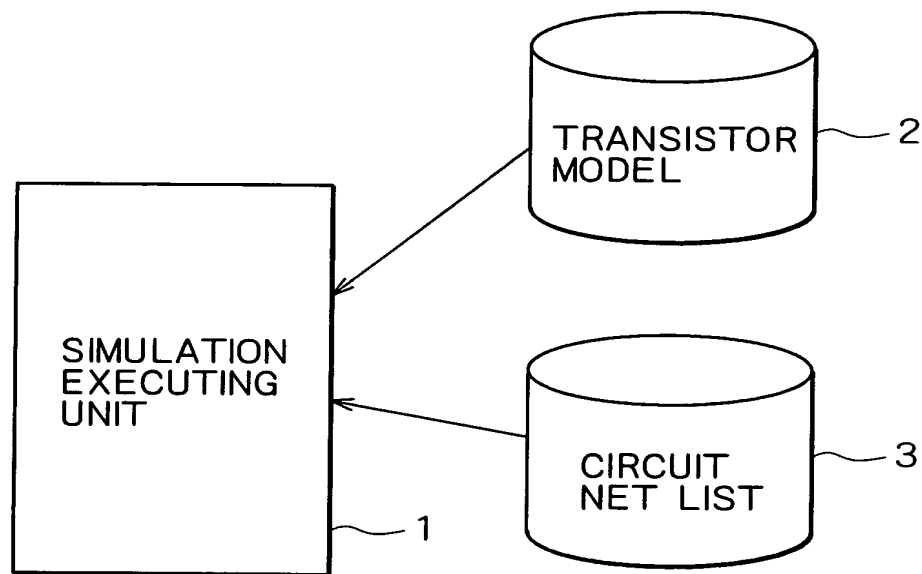


FIG. 2A (PRIOR ART)

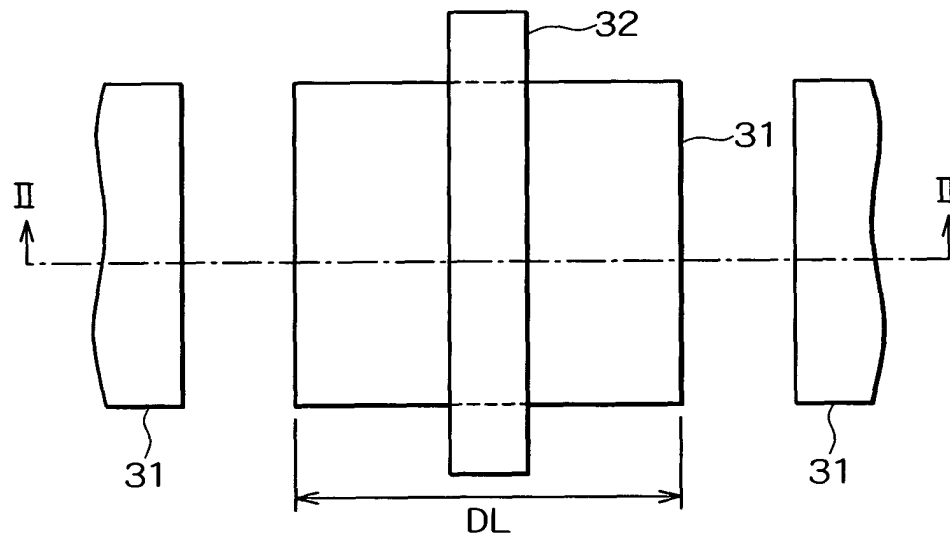
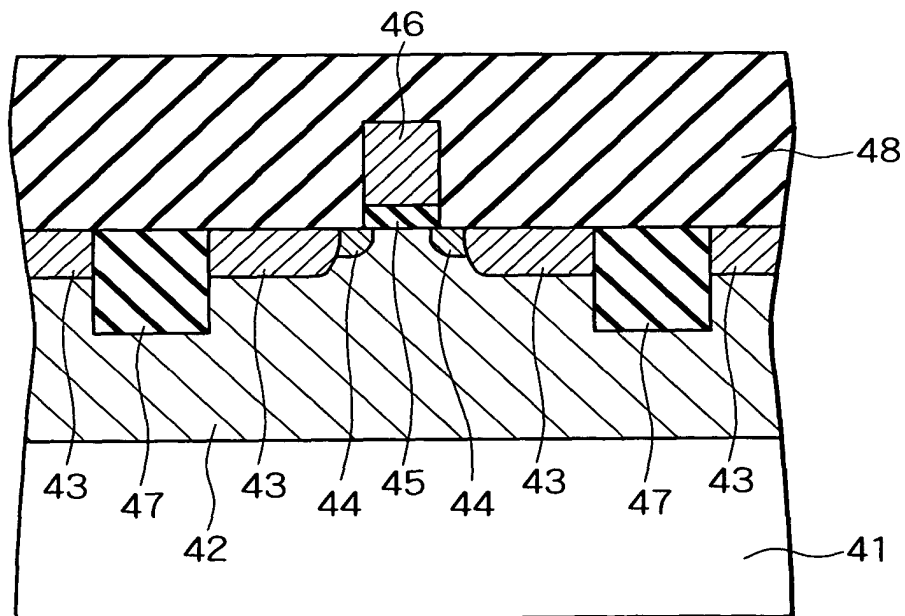


FIG. 2B (PRIOR ART)



Title: CIRCUIT SIMULATION APPARATUS  
INCORPORATING DIFFUSION LENGTH  
DEPENDENCE OF TRANSISTORS AND  
METHOD FOR CREATING TRANSISTOR  
MODEL

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FIG.3 (PRIOR ART)

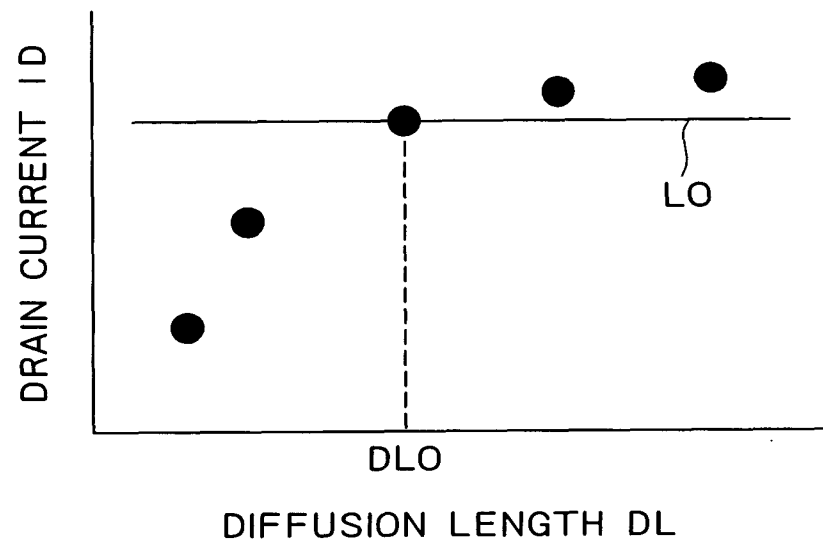
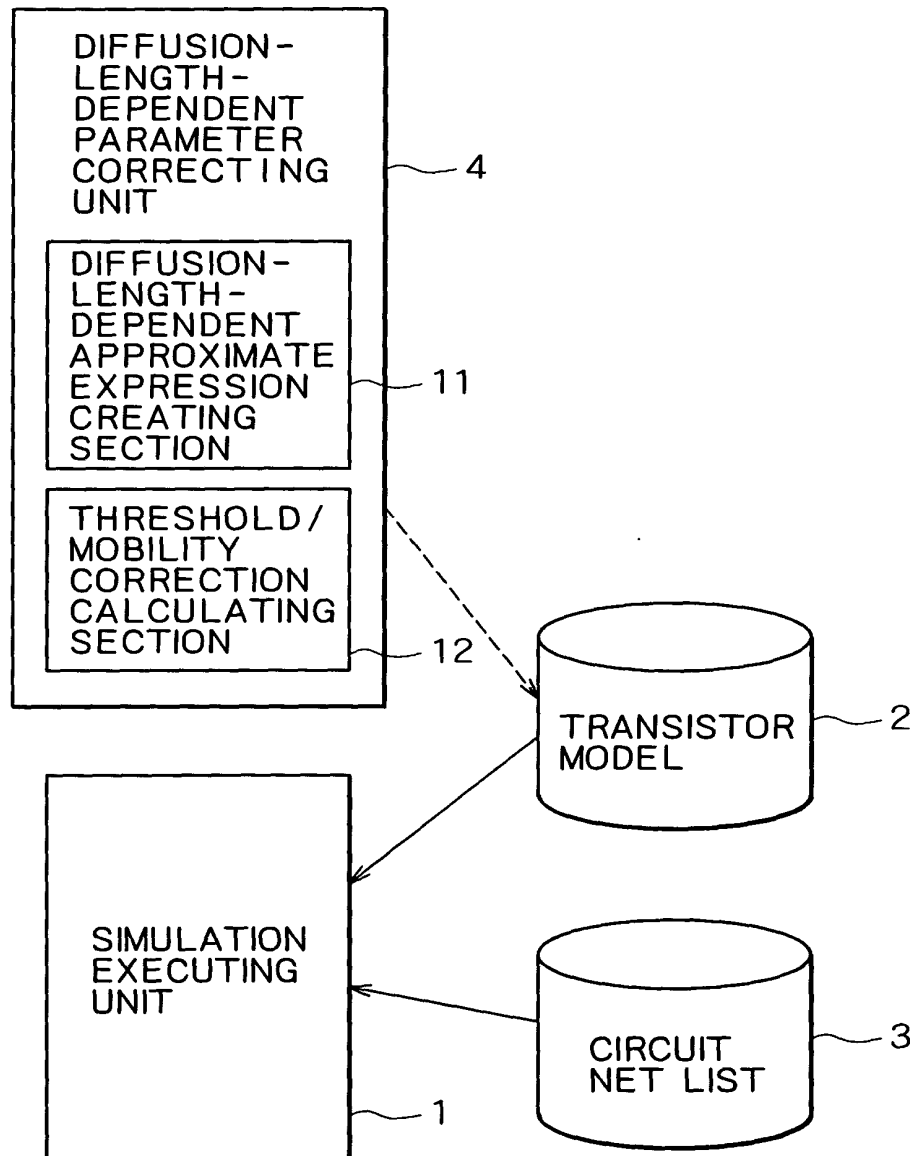


FIG. 4



## FIG. 5

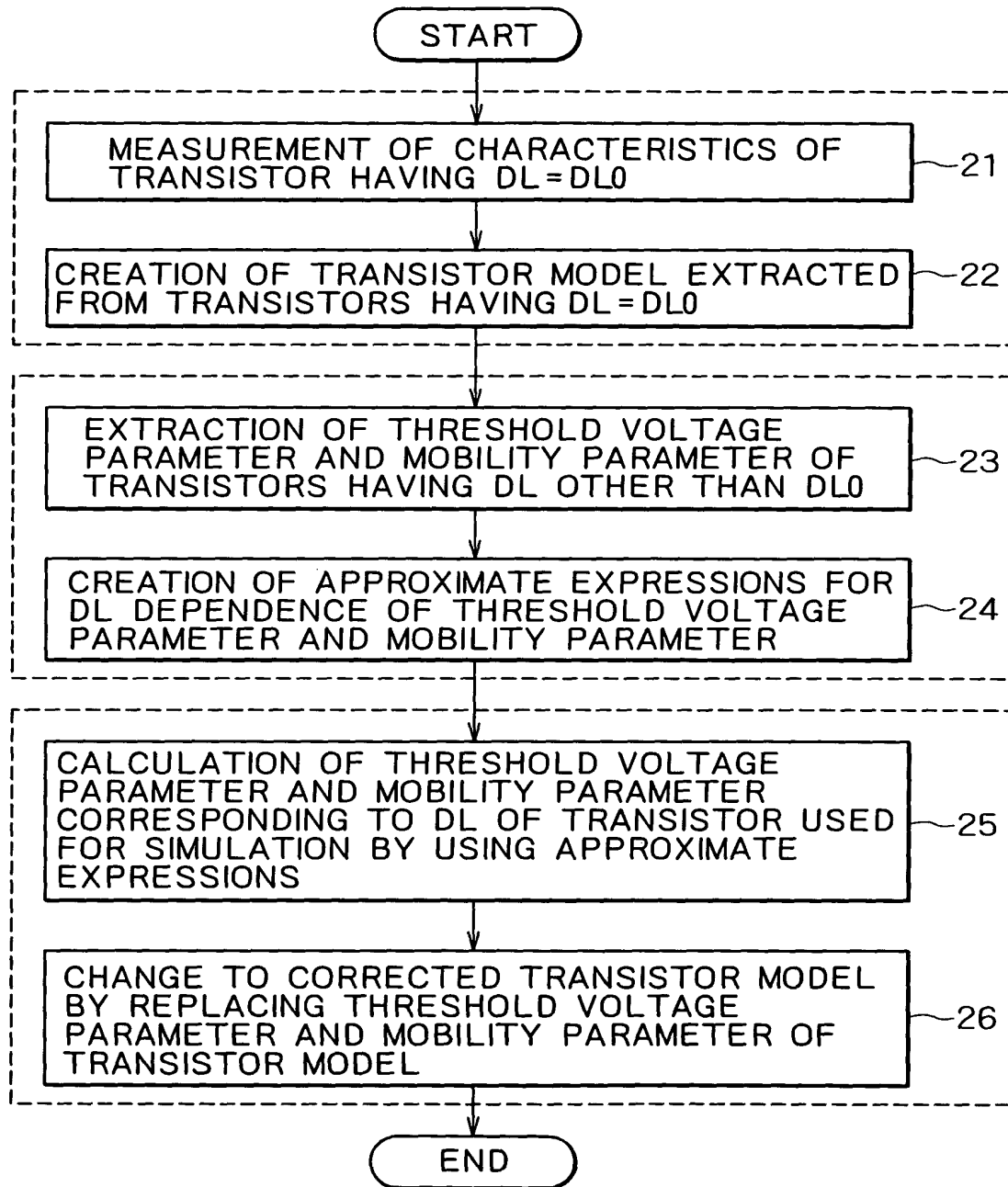


FIG. 6A

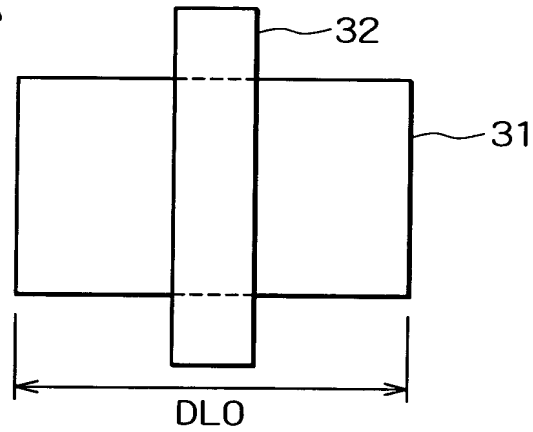


FIG. 6B

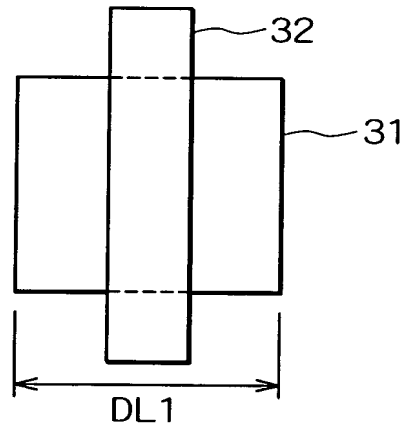


FIG. 6C

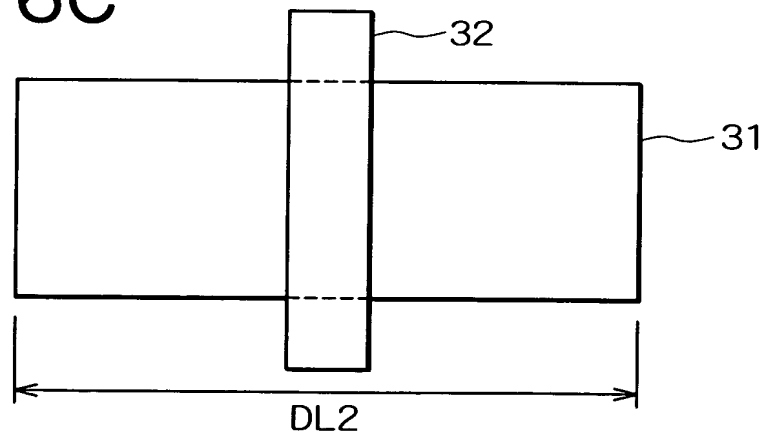


FIG.7A

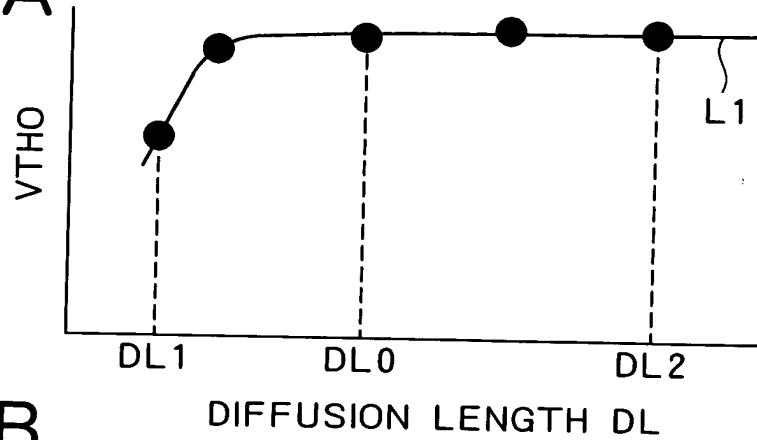


FIG.7B

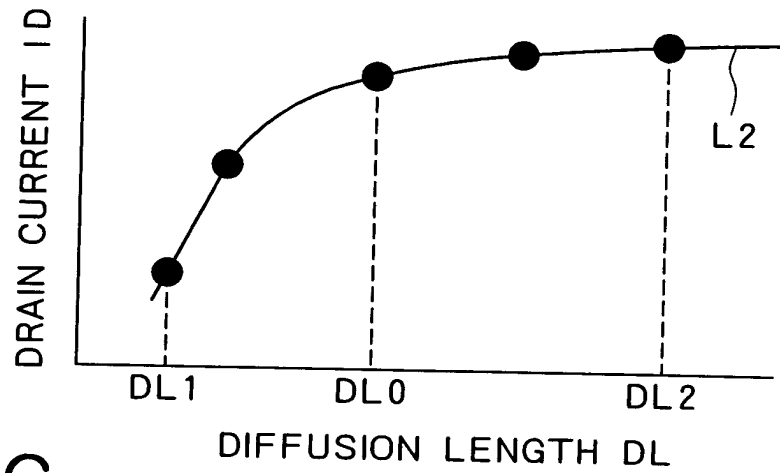


FIG.7C

